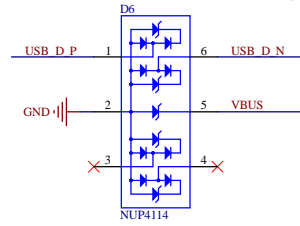
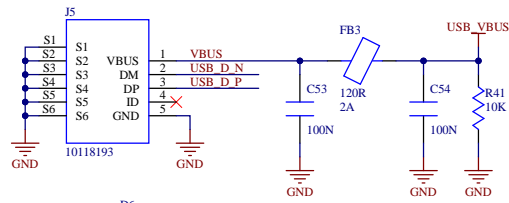


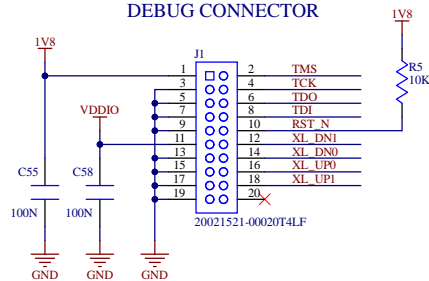
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USB CONNECTOR

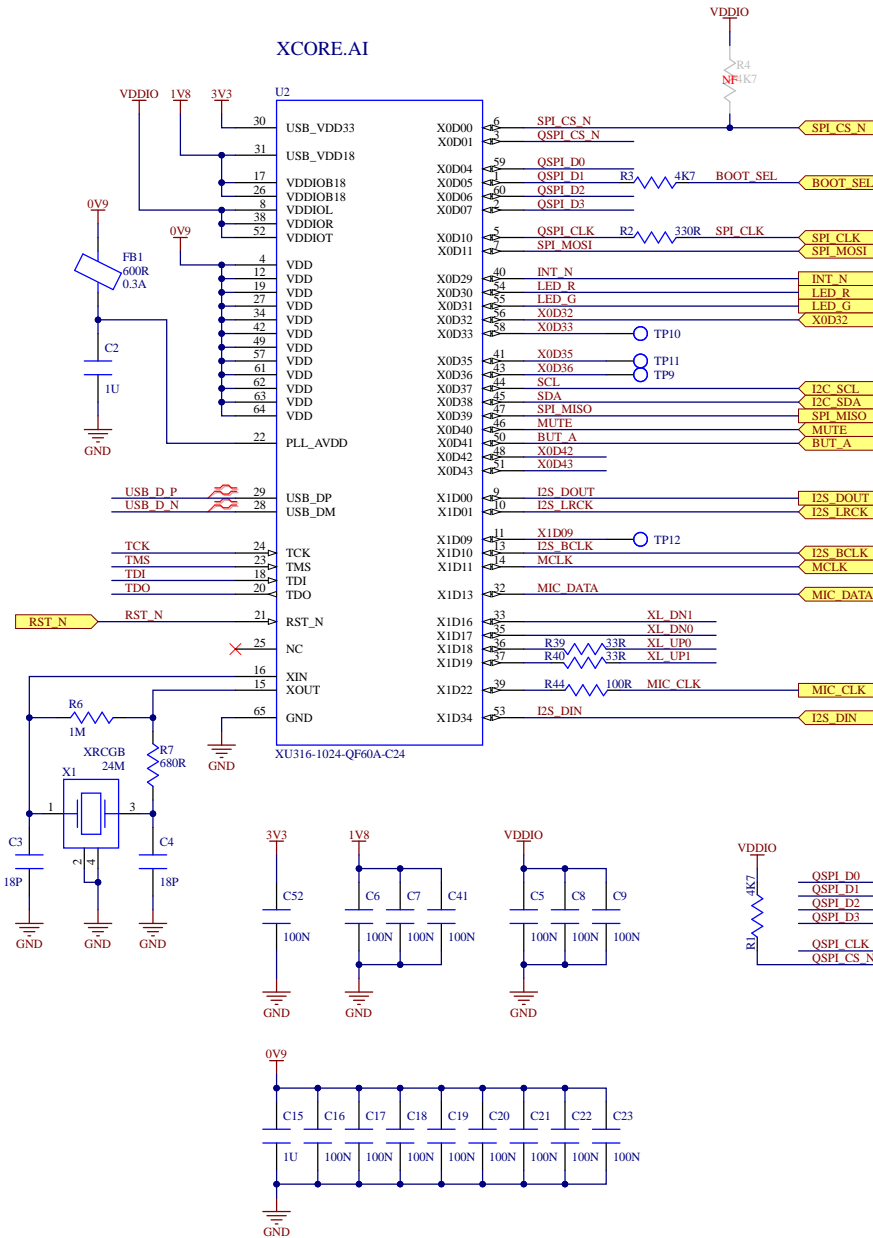


USB SYSTEMS ONLY

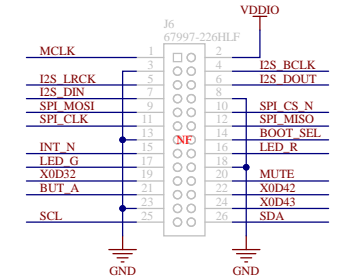
DEBUG CONNECTOR



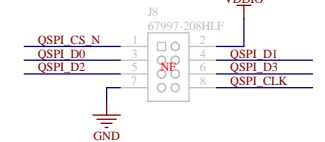
XCORE.AI



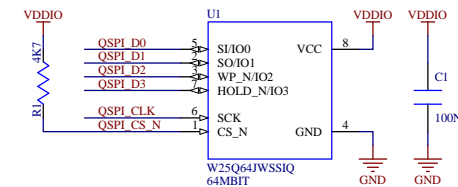
TESTPOINT/EXPANSION



QSPI FLASH



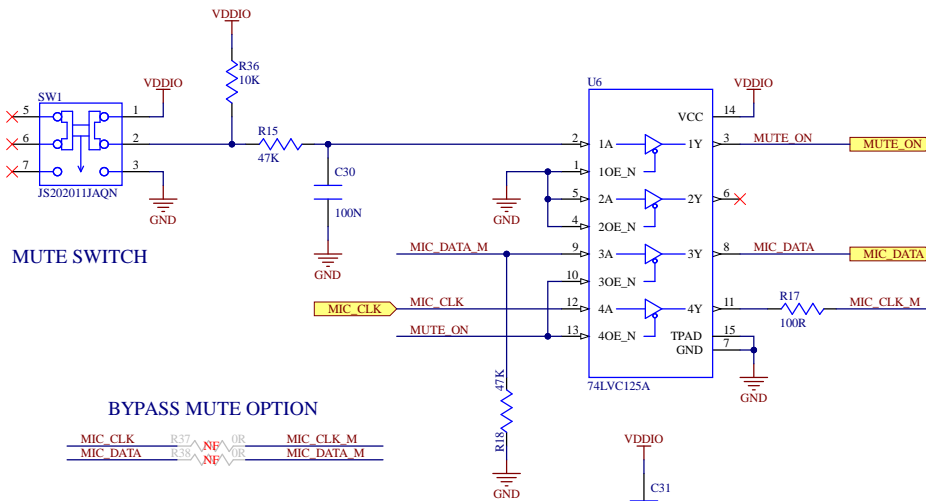
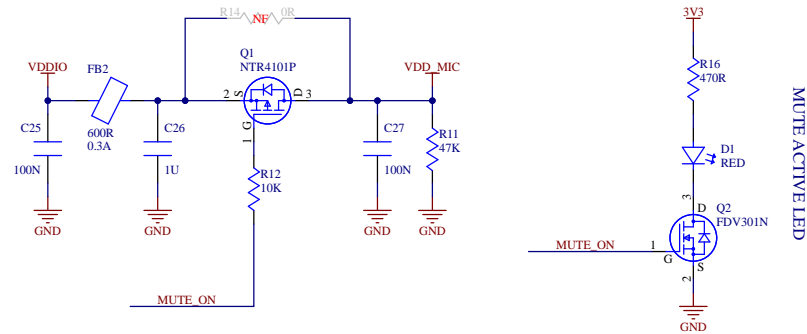
QSPI FLASH



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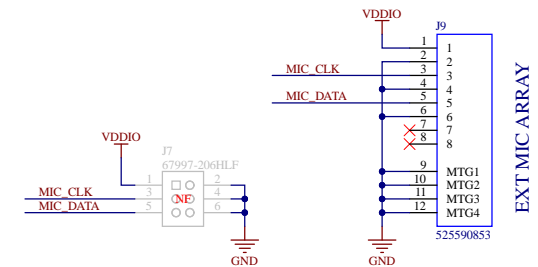
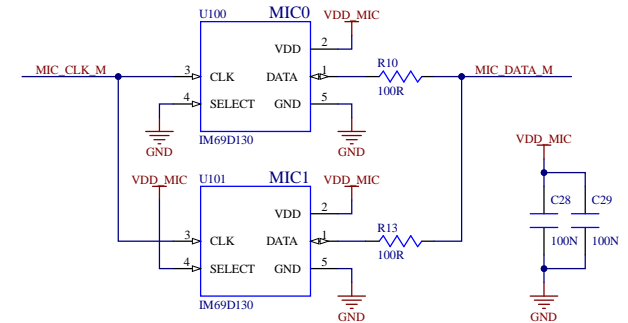
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MIC POWER SWITCH



MICROPHONE HARDWARE MUTING/SWITCHING
OPTIONAL FOR SYSTEMS REQUIRED TO SUPPORT
THIS SECURITY FEATURE

MICROPHONES



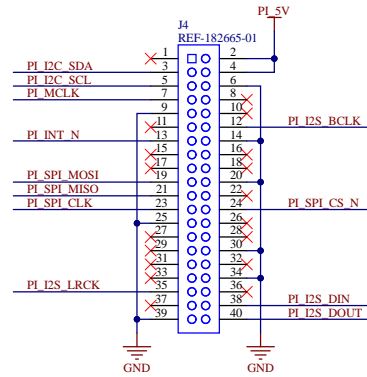
SET MUTE ACTIVE IF USING EXTERNAL MICS



Project Name
xcore.ai Voice Reference Design

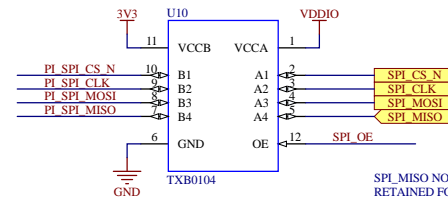
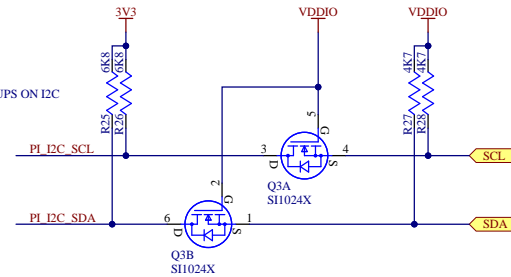
Build A	Sheet Name Mics Mute.SchDoc	Rev 1V0
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RASPBERRY PI HOST INTERFACE (3V3 LEVELS)



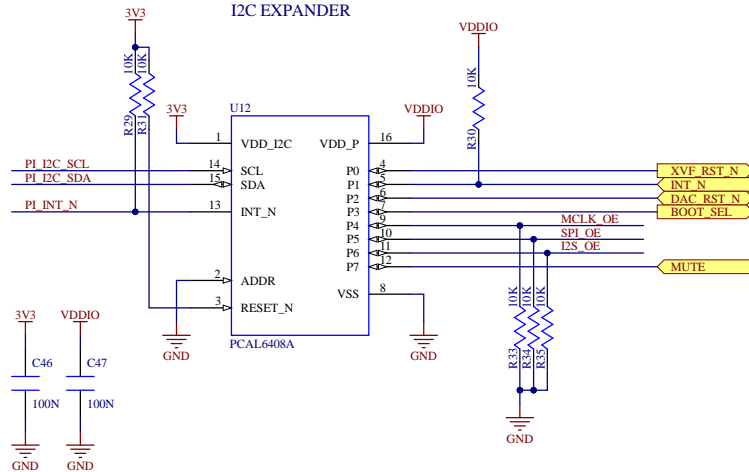
LEVEL SHIFTING

PI ALREADY HAS 1K8 PULLUPS ON I2C



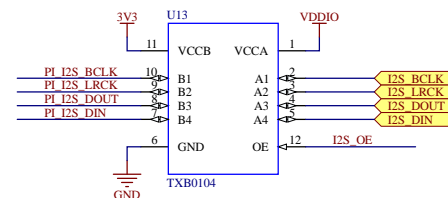
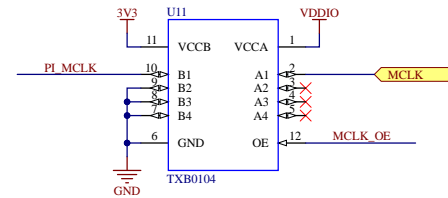
SPL_MISO NOT REQUIRED FOR SPI SLAVE BOOT
RETAINED FOR COMPLETENESS

I2C EXPANDER

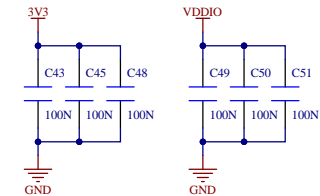


I2C ADDR = 0b0100000 = 0x20

INT_N IS OPEN DRAIN OUTPUT (NO DIODE TO SUPPLY)



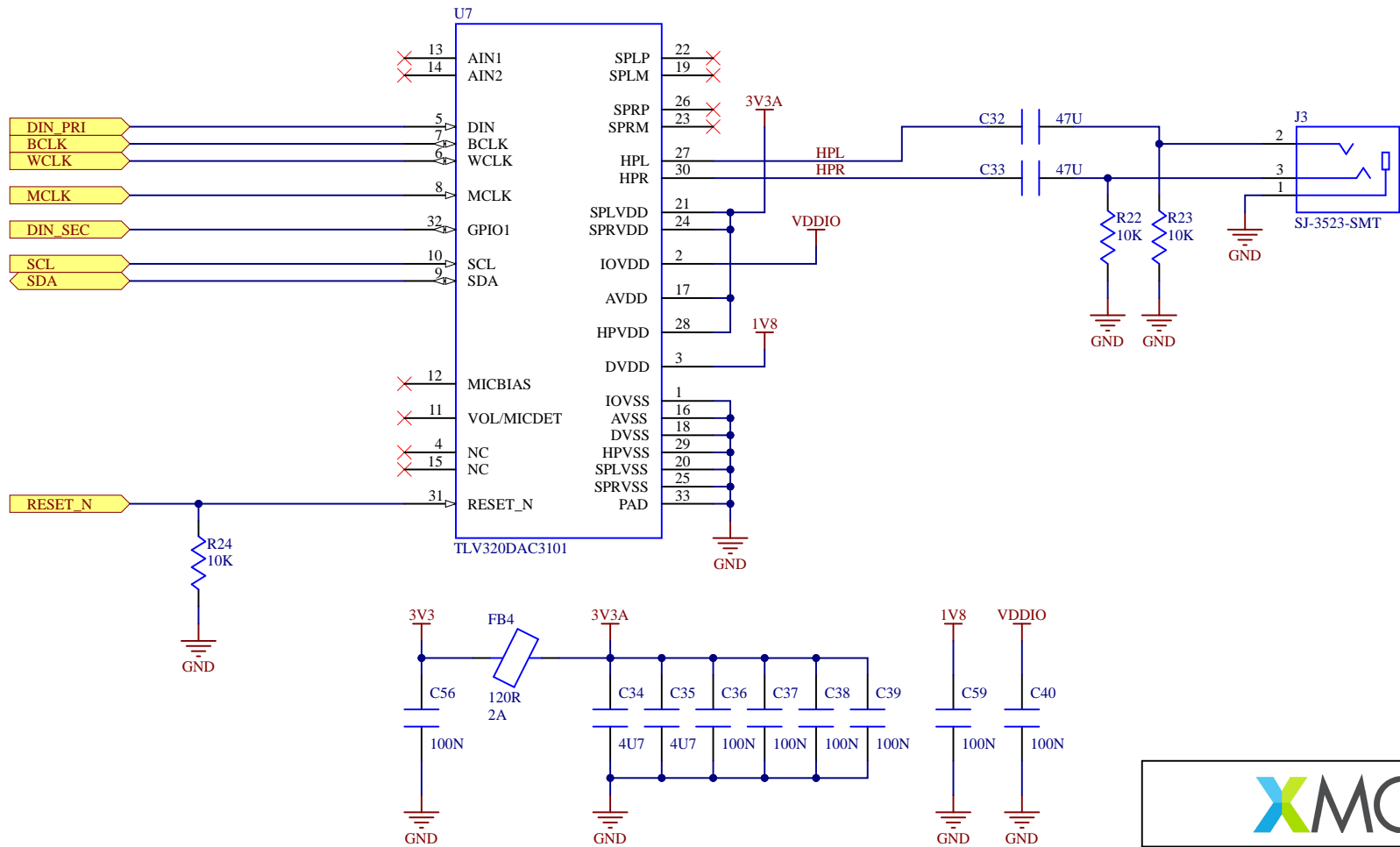
OE = 0 => A AND B HI-Z



XMOS

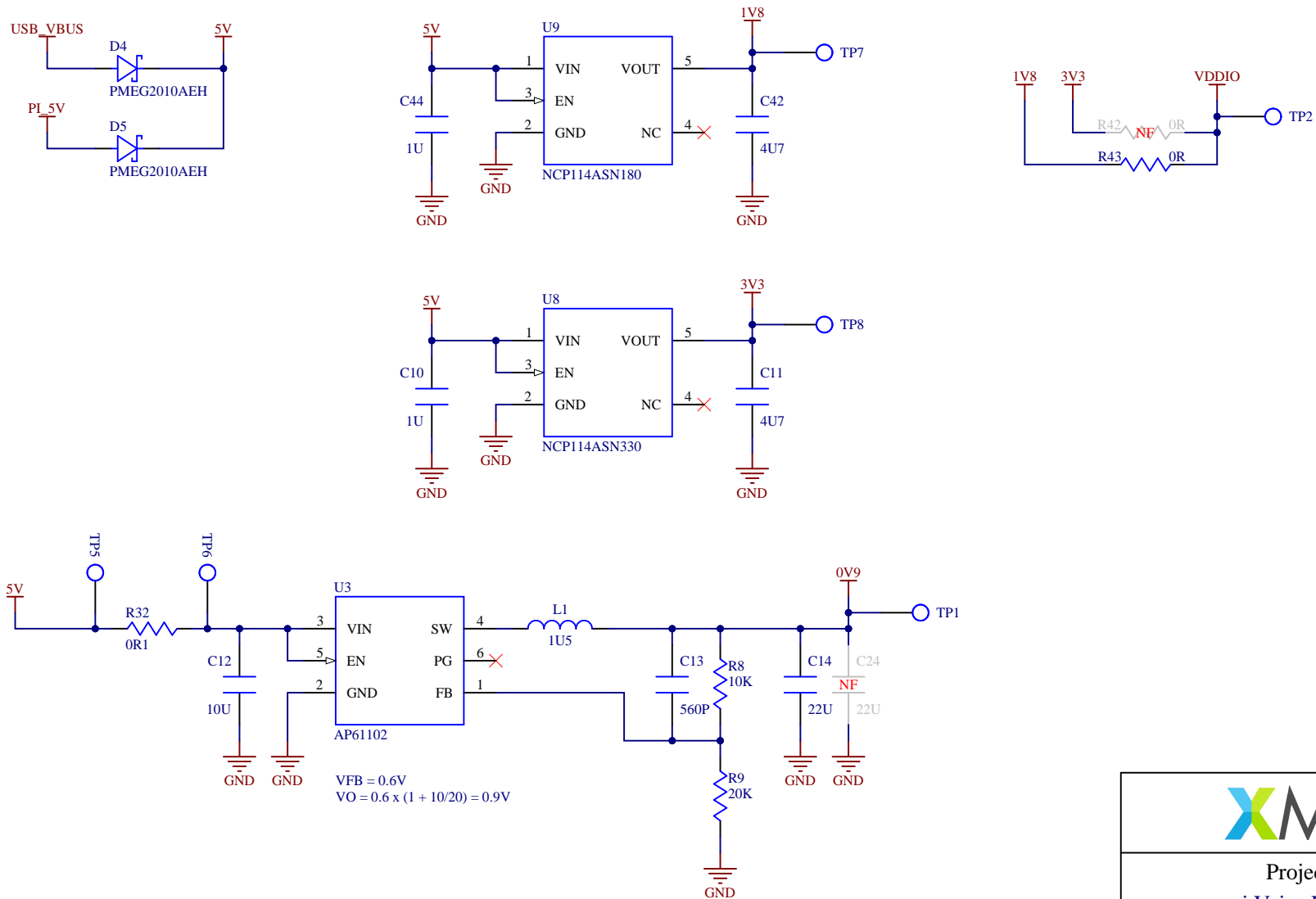
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