
Application Note: AN00185

Boot an xCORE-200 device from QuadSPI flash memory

This application note shows how to program an application into QuadSPI flash memory and therefore boot the application from QuadSPI flash memory on an XMOS xCORE-200 device.

Required tools and libraries

- xTIMEcomposer Tools Suite version 14.0 or later is required.

Required hardware

This application note is designed to run on an XMOS xCORE-200 series device.

The example code provided with the application has been implemented and tested on the xCORE-200 explorerKIT core module board but there is no dependency on this board and it can be modified to run on any development board which uses an xCORE-200 series device.

Prerequisites

- This document assumes familiarity with the XMOS xCORE-200 architecture, the XMOS tool chain and the xC language. Documentation related to these aspects which are not specific to this application note are linked to in the *References* appendix.
- This document assumes familiarity with QuadSPI flash memory, the xCORE quadflash library and the XMOS tool XFLASH.
- For descriptions of XMOS related terms found in this document please see the XMOS Glossary¹.
- The XMOS tools manual contains information regarding the use of xCORE devices².

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¹<http://www.xmos.com/published/glossary>

²<http://www.xmos.com/published/xtimecomposer-user-guide>